IN THE SPECIFICATION

Please replace paragraph [0058] with the following rewritten paragraph:

[0058]   The mask bit process takes a logical product between the mask bit and the result of a comparison between the MAC address of the section header and the MAC address in the key table 37. Specifically, the following relation represents the process carried out for each bit in the range of $0 \leq k \leq 47$:

$$(\sim(MR_1 \char`\^ MAC_1(k))) \ \& \ MASK_1(k) \tag{1},$$

where $\char`\^$ represents an exclusive OR operation, & represents a logical product, MR_1 is the MAC address read from the session header and stored in the MR register, $MAC_1(k)$ is the k-th MAC address stored in the key table, and $MASK_1(k)$ is the k-th mask value stored in the key table. When the logical product is "0", the masked portions of the two MAC addresses are identical.